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T-470 P003/004 F-069

APR 27 2005

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April 27, 2005	(Date)

04/28/2005 EABUBAK2 00000026 502257 10629512

01 FC:1501 1400.00 DA

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/629,512	07/29/2003	Zheng (Jeff) Chen	M-15197 US	6913

TITLE OF INVENTION: MEMORY ACCESS VIA SERIAL MEMORY INTERFACE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	05/11/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
CHO, JAMES HYONCHOL	2819	326-041000

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Lattice Semiconductor Corporation Hillsboro, Oregon

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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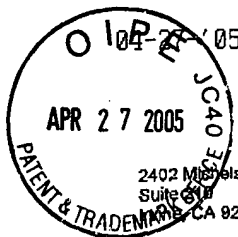
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T-470 P001/004 F-069

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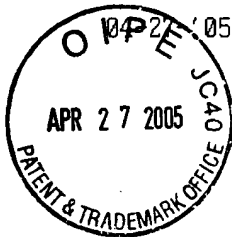
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From:	Greg J. Michelson	Date Sent:	
Subject:	Issue Fee for: Zheng (Jeff) Chen Serial No. 10/629,512 Filing Date: July 29, 2003	Time Sent:	
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Zheng (Jeff) Chen; Fulong Zhang; Harold Scholz
Assignee: Lattice Semiconductor Corporation
Title: Memory Access Via Serial Memory
Serial No.: 10/629,512 Filing Date: 07/29/2003
Examiner: Unassigned Group Art Unit: 2816
Docket No.: M-15197 US

Irvine, California
April 27, 2005

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Tina Kavanaugh

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1762 TECHNOLOGY DRIVE
SUITE 226
SAN JOSE, CA 95110
(949) 752-7040
FAX (408) 392-9262

MacPherson Kwok Chen & Heid LLP
1762 Technology Drive
Suite 226
San Jose, California 95110
Telephone: (949) 752-7040
Fax: (408) 392-9262